

**Display device driving circuitry and method**

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**Abstract**

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PCT No. PCT/GB95/00819 Sec. 371 Date Oct. 24, 1996 Sec. 102(e) Date Oct. 24, 1996 PCT Filed Apr. 10, 1995 PCT Pub. No. WO95/28696 PCT Pub. Date Oct. 26, 1995A method and apparatus for driving a display device including a matrix array of deflectable mirror devices is described. The deflectable mirror devices are divided into blocks of N rows. Selected groups of mirror devices within each block are loaded with single bit data in a cycle of loading operations During at least some of the loading operations, two series of reset cycles are used enabling one single bit cycle in one group to terminate and another single bit cycle in another group to commence within a single loading operation. Active bit data may be loaded and displayed in the otherwise unused time intervals between the active bits.

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